



# Centre for Multi-Disciplinary System Research Anna University, Chennai – 25



**Two- and Four-week Internship Program (Online and Offline) on  
“VLSI Design for Basic Research – FPGA Design Flow” for  
UG, PG Students, Research Scholars, Faculty and Industry Personals  
Coordinator: Dr. J. Kamala, Prof. DECE, Director CMDSR**

### Topics to be covered

- ✓ FPGA building block architectures
- ✓ Static Timing analysis
- ✓ SoC Design
- ✓ Implementation
- ✓ Verilog & System Verilog coding
- ✓ Device programming
- ✓ Design entry
- ✓ IP cores
- ✓ Simulation and verification
- ✓ Hands-on sessions with FPGA
- ✓ Synthesis
- ✓ Guided Project

Internship Details	Theory (Hours)	Practical (Hours)	Guided Project	Internship Fee
2 Weeks Program (23 Dec 24 – 3 Jan 25) (online & offline)	30	30	-	Rs. 5310/- (inclusive of GST)
4 Weeks Program (23 Dec 24 – 3 Jan 25) (online & offline)	30	30	60 Hours (Flexible Period)	Rs. 9440/- (Inclusive of GST)

Interested candidates have to register for the internship using the google form link <https://forms.gle/DM1dUdS64q1Q7eNn6>

The participant has to upload college ID / work place ID and Payment receipt in the form.

**Internship fee may be paid through**

**UPI to account number: 37614464781 (please mention participant name during payment)**

**or Demand Draft** drawn in favor of **“THE DIRECTOR, CSRC, ANNA UNIVERSITY, CH-25”**

The Registration Form and DD/UPI Payment receipt to be posted to:

**“The Director, Center for Multi-Disciplinary System Research,  
Department of ECE, College of Engineering Guindy,  
Anna University, Chennai - 600025”**

Fee does not include Lunch. Accommodation at Anna University Campus will be available at additional charges, on prior request.



**Centre for Multi- Disciplinary Systems Research (CMD SR)** has been formed as an autonomous research Centre. The centre has high potential to establish mutual collaborations with various industries and National statutory bodies which support its advanced research in the field of VLSI Design, Artificial Intelligence, Signal Processing, Embedded Systems, Cryptography, Electronics System Design, PV Electronics and Quantum Computing.

## Contact us

**Centre for Multi-Disciplinary System Research (CMD SR)**  
**No. 315/316, Second Floor, Centre for Excellence Building**  
**College of Engineering Guindy (CEG)**  
**Anna University, Chennai 600 025**  
+91 9444148847, +91- 044- 22358898  
cmdsorceg2022@gmail.com / dircmdsr@annauniv.edu  
www.annauniv.edu/cmdsr

## Internship Registration link

<https://forms.gle/DZGdc1xoHuSn6drZA>

## Important Dates

**Last date for registration – 21.12.2024**

**Internship starts from – 23.12.2024**



Online participants will be joining through online-meet with interactive hands-on sessions and can access our facility virtually. Offline participants are requested to bring your laptops for practical sessions. Minimum number of candidates required to conduct the course is 10.